

C.U.SHAH UNIVERSITY

Winter Examination-2018

Subject Name : Computer Organization and Architecture

Subject Code : 4CS03COA1

Branch: M.Sc.C.A. & I.T. (Integrated)

Semester : 3

Date : 27/11/2018

Time : 02:30 To 05:30

Marks : 70

Instructions:

- (1) Use of Programmable calculator & any other electronic instrument is prohibited.
 - (2) Instructions written on main answer book are strictly to be obeyed.
 - (3) Draw neat diagrams and figures (if necessary) at right places.
 - (4) Assume suitable data if needed.
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Q-1

Attempt the following questions:

(14)

- a) 1 byte = _____ bit.
A. 8 B.16 C.32 D. 64
- b) In bollean algebra $A+A' =$ _____
A.1 B.0 C. 2 D. 4
- c) In bollean algebra $AA' =$ _____
A. 1 B. 0 C.2 D.4
- d) In bollean algebra $A+A =$ _____
A. A B. 0 C. A' D. None of these
- e) In bollean algebra $A+1 =$ _____
A.1 B. 0 C. 2 D. 4
- f) Define term : Digital electronics
- g) Define term : Analog electronics
- h) _____ is types of RAM.
A. Static RAM B.Direct RAM C. Symmetric RAM D. None of these
- i) _____ is types of ROM.
A. EPROM B. EDPR0M C.ETPR0M D. None of these
- j) Define term : Computer
- k) Define term : Hardware
- l) Half adder is _____ circuit.
A. Sequential B. Combinational C. both D. None of these
- m) T- Flipflop is _____ circuit.
A. Sequential B.Combinational C. both D.None of these
- n) Register is _____ device.
A. Sequential B. Combinational C. Storage D. None of these



